



**BLOCK C:**    **C 1**    **C2**    (select your choice and the optional exams)

**A.Y. 2024-25**

Study Plan - Automatic Acceptance

ID STUDENT

e-mail

FAMILY NAME

First Name

Your Bachelor's degree

1st year

2nd year

Year of enrollment

Y	s	SUBJECTS	CFU	SSD	BLOCK	C
1	I	INTEGRATED SENSORS	9	ING-INF/01		9
1	I	ROBOT MECHANICS	9	ING-IND/13		9
1	II	NANOTECHNOLOGY	6	ING-INF/01		6
1	II	POWERTRAIN TECHNOLOGIES FOR FUTURE MOBILITY	9	ING-IND/08		9
1	II	VLSI CIRCUIT AND SYSTEM DESIGN	9	ING-INF/01		9
1	I	<b>INTERNSHIP</b>	6			6
2	I	CONTROL OF MECHANICAL SYSTEMS	9	ING-INF/04		9
2	I	ELECTRONICS OF IOT AND EMBEDDED SYSTEMS	12	ING-INF/01		12
		M-5519 – ELECTRONICS OF IOT				
		M-5520 – DESIGN OF EMBEDDED SYSTEMS FOR MECHATRONICS				
2	II	COMPUTER VISION	6	ING-INF/07		6
2	II	POWER ELECTRONICS AND ELECTRICAL DRIVES	9	ING-INF/01		9
<b>BLOCK C1 - MECHATRONICS SYSTEMS AND ICT - LEARNING AND COMMUNICATION</b>						
1	I	INNOVATIVE MATERIALS WITH LABORATORY	6	ING-IND/21	C1	6
2	II	CONTROL OF ELECTRICAL MOTORS AND VEHICLES	6	ING-INF/04	C1	6
1	II	Digital Signal Processing	6	ING-INF/03	C1	6
2		Deep Learning and Applications		ING-INF/01	C1	
		Identification and neural networks		ING-INF/04	C1	
2	I	Digital Communications	6	ING-INF/03	C1	6
		Information Theory and Data Science			C1	
		Multimedia Processing and Communication			C1	
<b>BLOCK C2 - MECHATRONICS SYSTEMS AND ICT - INTERCONNECTED ELECTRIC VEHICLE ENGINEERING</b>						
1	II	INTEGRATED SOLUTIONS FOR SUSTAINABLE MOBILITY AND ENERGY	6	ING-IND/08	C2	6
2	II	CONTROL OF ELECTRICAL MOTORS AND VEHICLES	6	ING-INF/04	C2	6
1	I	On Board Energy Generation and Storage	6	ING-INF/01	C2	6
		Wireless Electromagnetic Technologies		ING-INF/02	C2	
2	II	Radar and Localization		ING-INF/03	C2	
1	II	Electric Propulsion	6	ING-IND/32	C2	6
		Digital Signal Processing		ING-INF/03	C2	
2		<b>FINAL EXAM</b>	12			12
<b>total CFU</b>				<b>120</b>		

Date of submission

Student's signature

Date of approval

School Office (*Simona Ranieri*)

The Coordinator (*Prof. C.M. Verrelli*)

Reserved to the Management office